

## **SPECIFICATION**

Please amend the title as follows:

**PREDICATE REGISTER FILE SCOREBOARDING AND RENAMING WRITE BY  
AN INSTRUCTION WITH A PENDING INSTRUCTION HAVING DATA DEPENDENCY**

Please insert on page 4 directly after paragraph [0012] the following new paragraph:

Figure 7 illustrates a technique to handle a data dependency using two predicate register files.

Please insert on page 18 between paragraphs [0035] and [0036] the following new paragraph.

Figure 7 illustrates a technique to handle a data dependency using two predicate register files. As illustrated in Figure 7, predicate values may be stored in a first register file including a first plurality of predicate registers for block 702. A first instruction may be executed to write predicate values to a second register file including a second plurality of predicate registers prior to commitment of the first instruction for block 704. A second instruction pending with the first instruction and having a data dependency with the first instruction may be executed to access the first register file prior to commitment of the first instruction for block 706. Execution of the second instruction for block 706 may occur simultaneously or out of order with execution of the first instruction for block 704.